



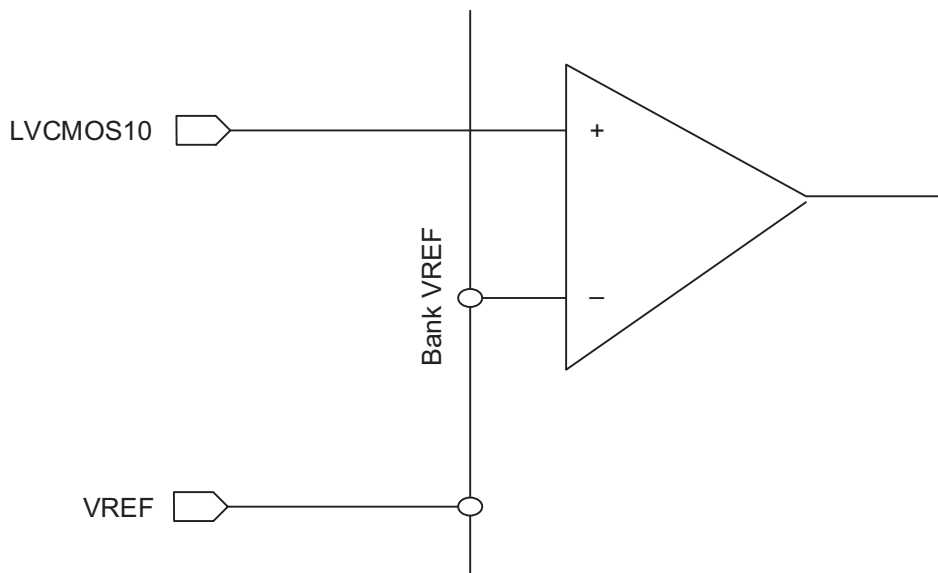
MachXO3™ Family Data Sheet Supplement for LVCMOS10 Inputs and BIDs

DS1047_S_LVCMOS10 Version 1.1, April 2016

Overview

This document is a supplement to the MachXO3 Family Data Sheet and provides the following additions or customizations:

- Support for LVCMOS10R33 and LVCMOS10R25 inputs and BIDs for –6 speed grade devices



sysIO Recommended Operating Conditions

Standard	V _{CCIO} (V)			V _{REF} (V)		
	Min.	Typ.	Max.	Min.	Typ.	Max.
LVC MOS10R33	3.135	3.3	3.6	0.35	0.5	0.65
LVC MOS10R25	2.375	2.5	2.625	0.35	0.5	0.65

sysIO Single-Ended DC Electrical Characteristics

Input/Output Standard	V _{IL}		V _{IH}		V _{OL} Max. (V)	V _{OH} Min. (V)	I _{OL} (mA)	I _{OH1} (mA)
	Min. (V)	Max. (V)	Min. (V)	Max. (V)				
LVC MOS10R33	-0.3	V _{REF} – 0.1	V _{REF} + 0.1	3.465	0.40	N/A Open Drain	16, 12, 8, 4	N/A Open Drain
LVC MOS10R25	-0.3	V _{REF} – 0.1	V _{REF} + 0.1	3.465	0.40	N/A Open Drain	12, 8, 4	N/A Open Drain

Revision History

Date	Version	Change Summary
April 2016	1.1	Corrected typo error in Overview section.
November 2015	1.0	Initial release.