



NOTES: UNLESS OTHERWISE SPECIFIED

1. DIMENSIONS AND TOLERANCES PER ANSI Y14.5M.

2. ALL DIMENSIONS ARE IN MILLIMETERS.

△ 3 DIMENSION "b" IS MEASURED AT THE MAXIMUM SOLDER BALL DIAMETER, PARALLEL TO PRIMARY DATUM C.

△ 4 PRIMARY DATUM C AND SEATING PLANE ARE DEFINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.

△ 5 BILATERAL TOLERANCE ZONE IS APPLIED TO EACH SIDE OF THE PACKAGE BODY.

△ 6 EXACT SHAPE AND SIZE OF THIS FEATURE IS OPTIONAL.

7. JEDEC REFERENCE: JEP95 DG4.18

	SYMBOL	COMMON DIMENSIONS		
		MIN.	NOM.	MAX.
Total Thickness	A	0.502	0.541	0.580
Stand Off	A1	0.164	0.194	0.224
Wafer Thickness	A2	0.322 ±0.025		
Backside Coating Thickness	A3	0.025		
Body Size	D	3.688 BSC		
	E	3.792 BSC		
Ball Diameter (Size)		0.250		
Ball/Bump Width	b	0.200	0.250	0.300
Ball/Bump Pitch	eD	0.400		
	eE	0.400		
Ball/Bump Count	n	81		
	D1	3.200 BSC		
Edge Ball Center to Center	E1	3.200 BSC		
Package Edge Tolerance	aaa	0.030		
Coplanarity (whole wafer)	ddd	0.030		
Ball/Bump Offset (Package)	eee	0.015		

Title: Package Outline Drawing

Pkg Type: WLCSP

Document No:

POD-250072

Product Family: LFMXO4

Pin Count: 81

Product Name: LFMXO4-050-UUG81

Pkg Size: 3.688x3.792 mm

Rev: A

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